

- Drafts
- Pending
- Active
  - L1: (445) processor same cell same (configurable or
  - L2: (35) l1 and (address same end same transfer)
  - L3: (3) l1 same (address same end same transfer)
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search List Browse Delete

DBs: USPAT

Default operator: OR

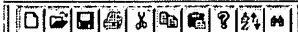
☒ Plurals☒ Highlight all hit terms initially

BRS form ISTR form Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	445	processor same cell same (configurable or	USPAT	2007/02/27 12:35			
2	BRS	L2	35	l1 and (address same end same transfer)	USPAT	2007/02/27 12:36			
3	BRS	L3	3	l1 same (address same end same transfer)	USPAT	2007/02/27 12:36			

Hits Details HTML

Ready



- ☐ Drafts
- ☐ Pending
- ☒ Active
  - ☒ L1: (445) processor same cell same (confi
  - ☒ L2: (35) 11 and (address same end same tr
  - ☒ L3: (3) 11 same (address same end same tr
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (0)
- ☐ UDC
- ☐ Queue
- ☐ Trash

Search     DBs: Default operator: ☒ Plurals☒ Highlight all hit terms initially

11 same (address same end same transfer)

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6697475 B1	20040224	49	System and method for implementing an end office	379/201.12	370/259; 370/352;		MeLampy; Patrick al.
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6477143 B1	20021105	23	Method and apparatus for packet network congestion	370/230	370/232; 370/235;		Ginossar; Dror
3	<input type="checkbox"/>	<input type="checkbox"/>	US 4888726 A	19891219	26	Distributed processing in a cluster of industrial	709/201	709/213; 709/215		Struger; Odo J. e

Ready